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Why Analog? Most of the physical signals are analog in nature! Although digital is great we need an analog interface to convert physical signals from analog to digital. Also, in some application after processing the signals in digital domain, we need to convert them back to analog.

## EECE488: Analog CMOS Integrated Circuit Design

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## RAZAVI-Solutions Design-of-Analog-Cmos-Integrated-Circuits

Course Outline MOS Device Structure and Circuit Models Single-Stage and Differential Amplifiers Passive and Active Current Mirrors Frequency Response of Amplifiers Noise Feedback Op Amp Design Stability and Frequency Compensation Bandgap References Introduction to Switched-Capacitor Circuits Simulation Platform: HSPICE or Cadence.

## Analog Integrated Circuit Design - Engineering

Layout of Analog CMOS Integrated Circuit Part 2 Transistors and Basic Cells Layout. F. Maloberti - Layout of Analog CMOS IC 2 Outline ... F. Maloberti - Layout of Analog CMOS IC 29 Layout Oriented Design M1 M2 M3 M4 M5 M6 M7 60 60 40 30 30 72 108 Possible stacks: 1 p-channel, 2 n-channel change the size of M6 and M7

## Layout of Analog CMOS Integrated Circuit - unipv

An alternative design that can resolve signals with accuracies much less than the input offset voltage of OpAmps is to use switched capacitors with clocks. Clock is a slightly advanced version of

## Analog Integrated Circuit Design 2nd Edition

Design of Analog CMOS Integrated Circuits Behzad Razavi Errata p. 6, Section 1.3, first sentence should read: The idea of metal-oxide-semiconductor ... p. 26, In Eq. (2.28), move  $1 + V_{DS}$  from the denominator to

the numerator. p.32, fifth line from top: change  $4jF$  to  $4j\hat{F}$ . p. 88, second line below Eq. (3.124) should read: ... roughly  $g_m r_{2O}$ .

### **Design of Analog CMOS Integrated Circuits - Engineering**

Supported by downloadable MATLAB code, and including over forty detailed worked examples, this book will provide professional analog circuit designers, researchers, and graduate students with the theoretical know-how and practical tools needed to acquire a systematic and re-use oriented design style for analog integrated circuits in modern CMOS.

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